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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	TOT CLAIMS	IND CLAIMS
10/579,911	04/09/2007	2827	1030	XA-10573	9	3

**CONFIRMATION NO. 6903**

181  
MILES & STOCKBRIDGE PC  
1751 PINNACLE DRIVE  
SUITE 500  
MCLEAN, VA22102-3833

**FILING RECEIPT**

Date Mailed: 08/21/2007

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections**

**Applicant(s)**

Satoru Hanzawa, Hachioji, JAPAN;  
Junji Shigeta, Fuchu, JAPAN;  
Shinichiro Kimura, Kunitachi, JAPAN;  
Takeshi Sakata, Hino, JAPAN;  
Riichiro Takemura, Tokyo, JAPAN;  
Kazuhiko Kajigaya, Iruma, JAPAN;

**Assignment For Published Patent Application**

Hitachi, Ltd.  
Elpida Memory, Inc.

**Power of Attorney:** The patent practitioners associated with Customer Number 181

**Domestic Priority data as claimed by applicant**

This application is a 371 of PCT/JP03/14901 11/21/2003

**Foreign Applications**

**If Required, Foreign Filing License Granted:** 08/18/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is  
**US10/579,911**

**Projected Publication Date:** 11/29/2007

**Non-Publication Request:** No

**Early Publication Request:** No

**Title**

Semiconductor Integrated Circuit Device

**Preliminary Class**

365

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